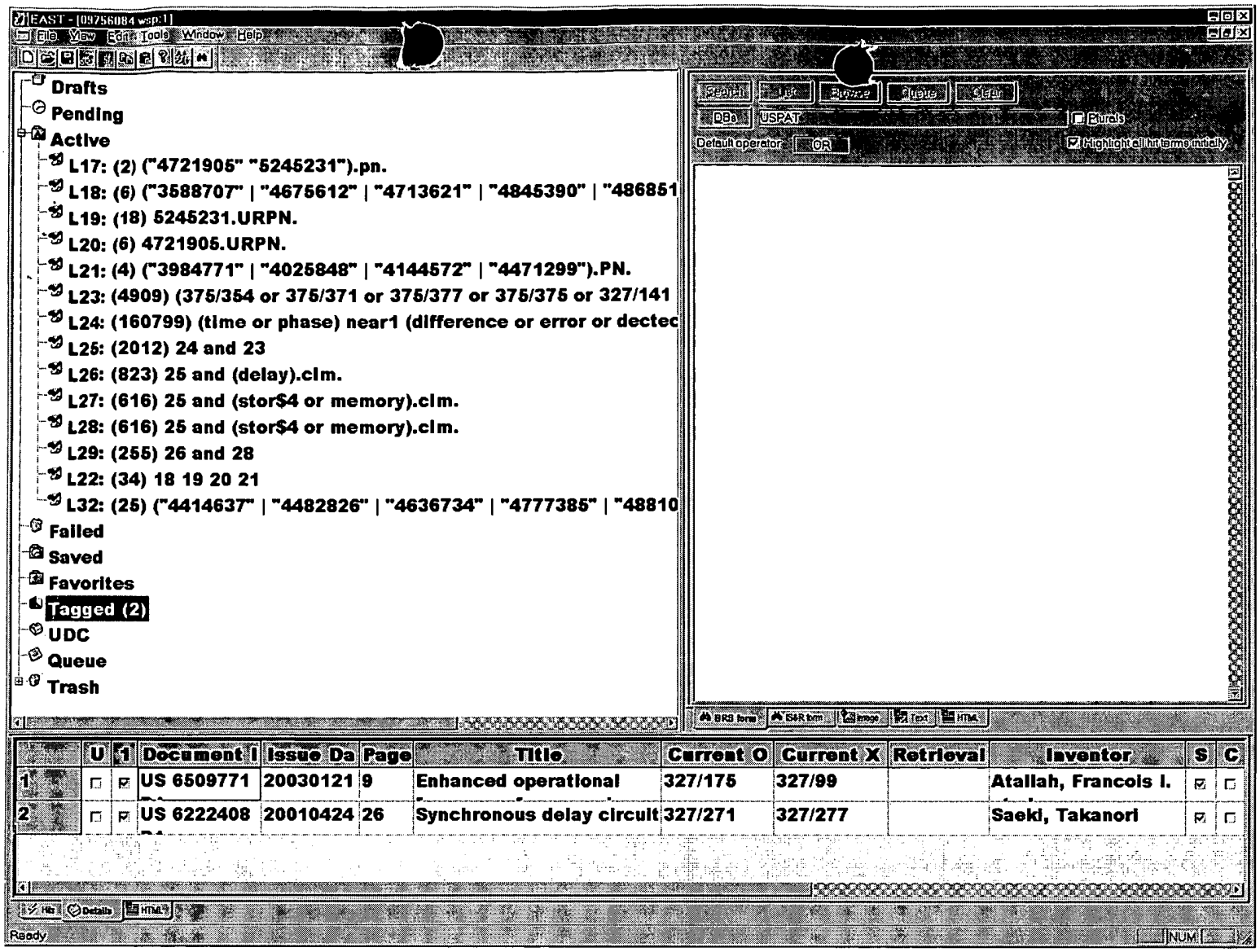
EAST - [09756084 wsp:1]
File View Edit Tools Window Help
Drafts
Pending
Active
L17: (2) ("4721905" "5245231").pn.
L18: (6) ("3588707" | "4675612" | "4713621" | "4845390" | "486851
L19: (18) 5245231.URPN.
L20: (6) 4721905.URPN.
L21: (4) ("3984771" | "4025848" | "4144572" | "4471299").PN.
L23: (4909) (375/354 or 375/371 or 375/377 or 375/375 or 327/141
L24: (160799) (time or phase) near1 (difference or error or dectec
L25: (2012) 24 and 23
L26: (823) 25 and (delay).clm.
L27: (616) 25 and (stor$4 or memory).clm.
L28: (616) 25 and (stor$4 or memory).clm.
L29: (255) 26 and 28
L22: (34) 18 19 20 21
L32: (25) ("4414637" | "4482826" | "4636734" | "4777385" | "48810
Failed
Saved
Favorites
Tagged (2)
UDC
Queue
Trash
Search
Clear
DBs
USPAT
Plurals
Default operator:
OR
Highlight all hit terms initially
BRS form
IS&R form
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Title
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Inventor
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C
1
US 6509771
20030121
9
Enhanced operational
327/175
327/99
Atallah, Francois I.
2
US 6222408
20010424
26
Synchronous delay circuit
327/271
327/277
Saeki, Takanori
Hits
Details
HTML
Ready
NUM